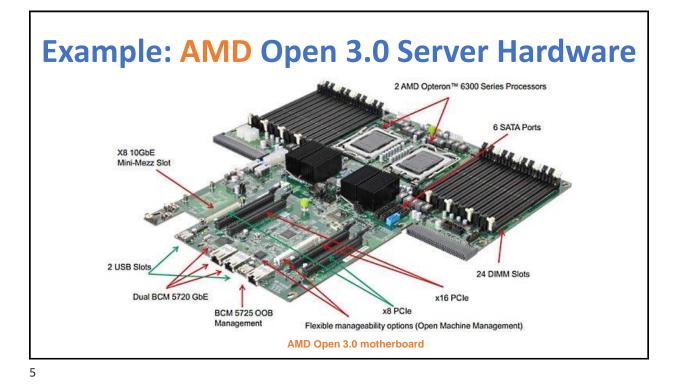


Example: FB Datacenter Racks



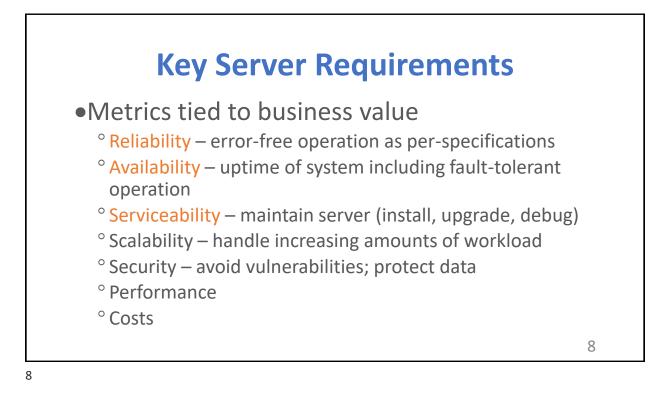


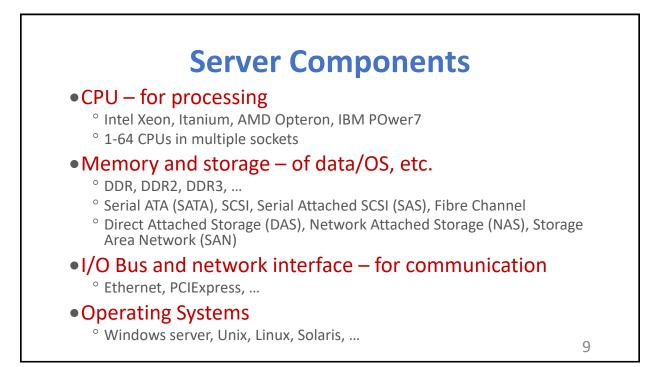




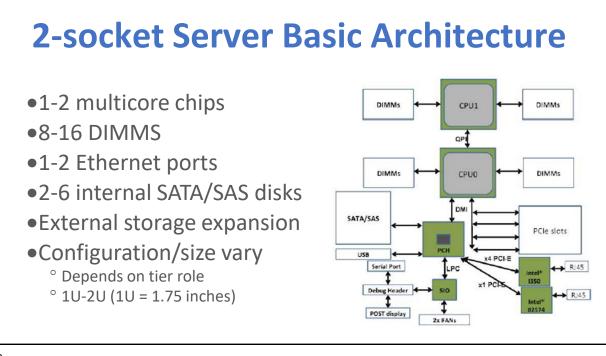
Desktop vs. Server

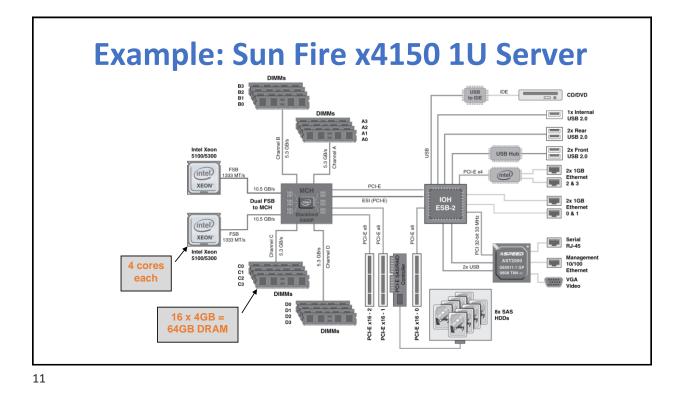
Desktop	Server
1-2 Desktop CPUs	Up to 64 server CPUs
192GB memory max	2 TB memory max
7 PCI/PCIe slots	Up to 192 PCIe slots
Fast high-res video	Basic video
Typically SATA disks	SAS, SATA, SSD, SCSI disks
Single user applications	Multi-user applications
Sound and multi-media	No sound systems
Monitor, keyboard, mouse	Shared/remote KVM
Designed for 9x5 operations	Designed for 24x7 operations
Little to no high-availability features	High availability and redundancy
Little to no manageability features	Support for manageability



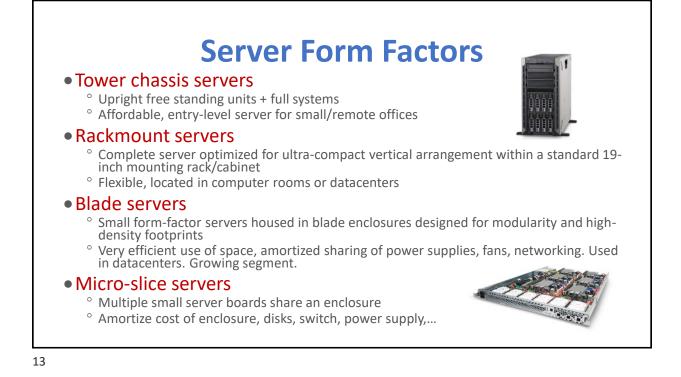


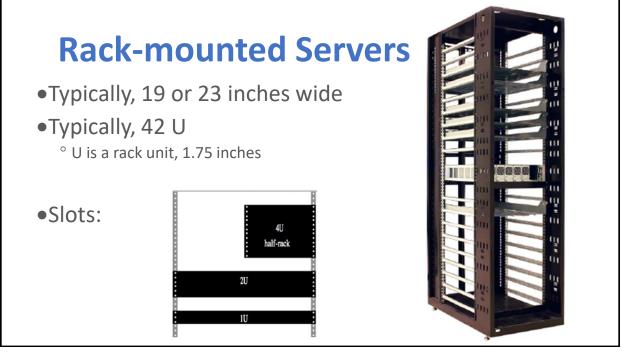


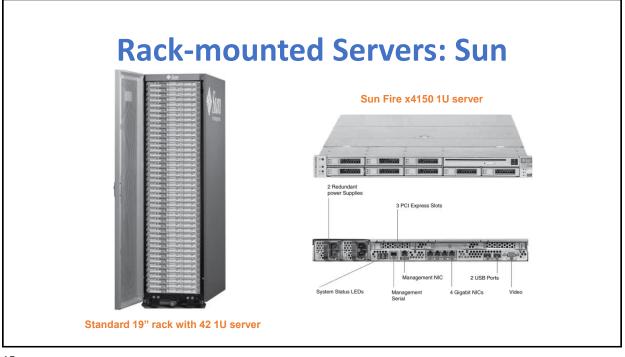




			option	Example Configurations					
	LAU	lible c	Julingu	Πατιστι	15				
Facebo	ook serve	er configura	ations for d	ifferent se	rvices				
		0							
Standard	1	Ш	IV	V	VI				
Systems	Web	Database	Hadoop	Haystack	Feed				
CPU	High 2 x E5-2670	Med 2 x E5-2660	Med 2 x X5650	Low 1 x L5630	High 2 x E5-2660				
		111-b	Medium	1.000	111-6				
Memory	Low 16GB	High 144GB	48GB	Low 18GB	High 144GB				
Disk	Low	High IOPS	High	High	Medium				
DISK	250GB	3.2 TB Flash	12 x 3TB SATA	12 x 3TB SATA	2TB SATA				
					Multifeed,				
Services	Web, Chat	Database	Hadoop	Photos, Video	Search, Ads				











Enclosure-level Density Optimization

Objective functions

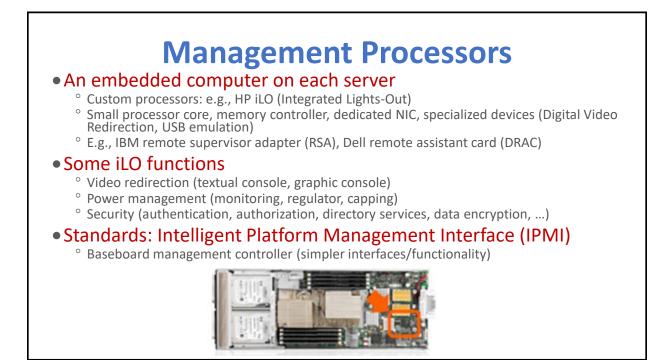
- Minimum costs min blade costs (max blades per enclosure to amortize costs) and min switch costs (number of internal and external ports in switches)
- ° Constrained by volume space within enclosure, minimum space required for server-class components, max power budget for server blade
- Maximum flexibility maximize switches for various network protocols, maximize performance of blades (highest power budget and volume) and switches (highest network speed protocols and highest external network connectors)

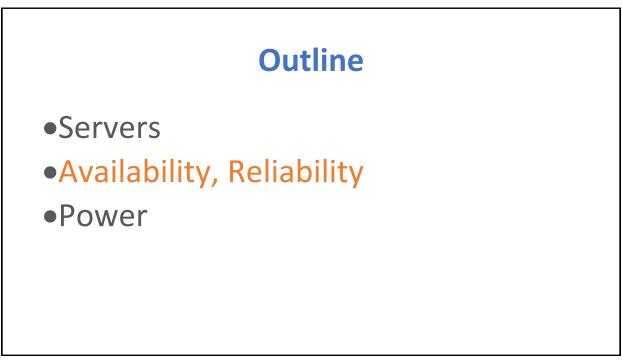
Multi-objective optimization across power envelope, per server volume space, switch bandwidth oversubscription ratio, network protocols, ...

17

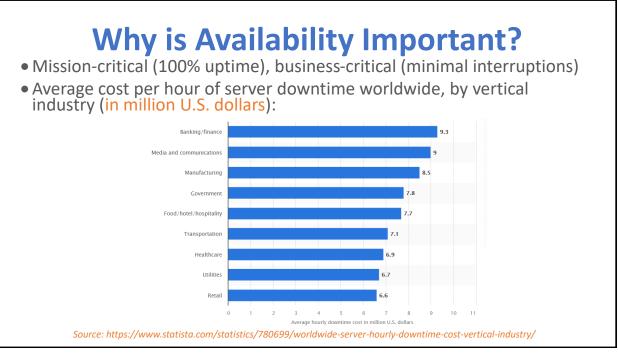
Platform (HW) Management Management tasks Turn on/off, recovery from failure (reboot after system crash), system events and alerts log, console (keyboard, video, and mouse (KVM)), monitoring (health), power management, installation (boot OS image) Platform management system

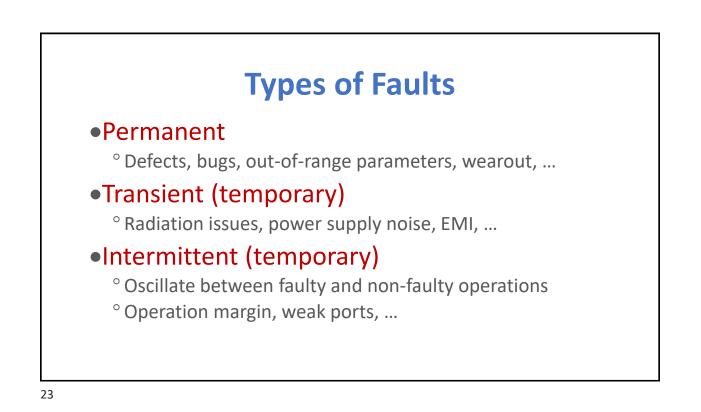
- ° Automates all these operations
- ° Out-of-Band (OOB), secure (privileged access point to the system), low-power (always on), flexible and low-cost

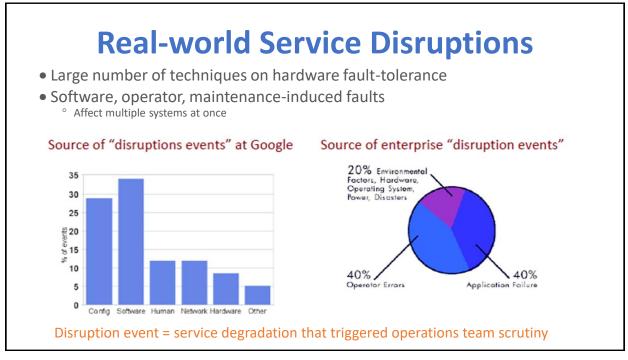


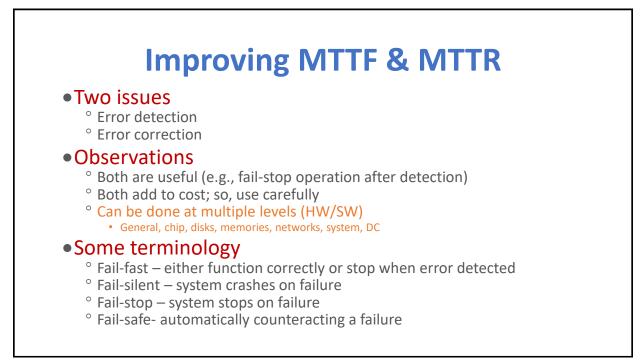


Availability Classifications				
Availability quoted in "9s"				
° E.g., Telephone system has five 9s availability				
$^\circ$ 99.999% availability of 5 minutes downtime per year				
Hattan	Bounding in an an			
Uptime	Downtime in one year			
99% (two 9's)	87.6 hours			
99.9% (three 9's)	8.76 hours			
99.99% (four 9's)	53 min			
99.999% (five 9's)	5 min			
99.9999% (six9's)	32 sec			

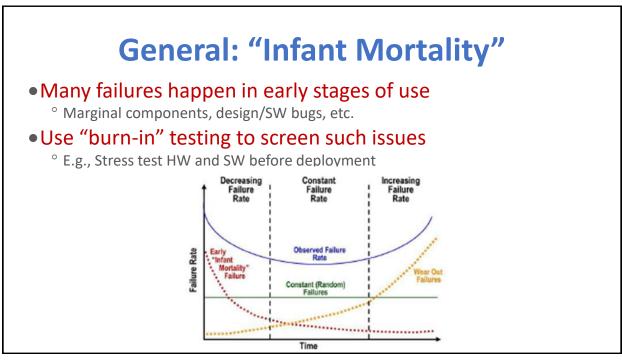


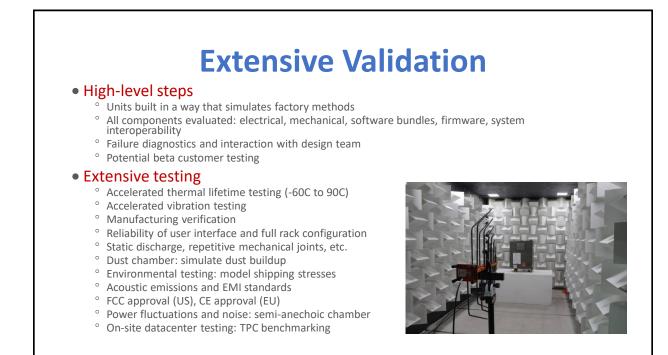




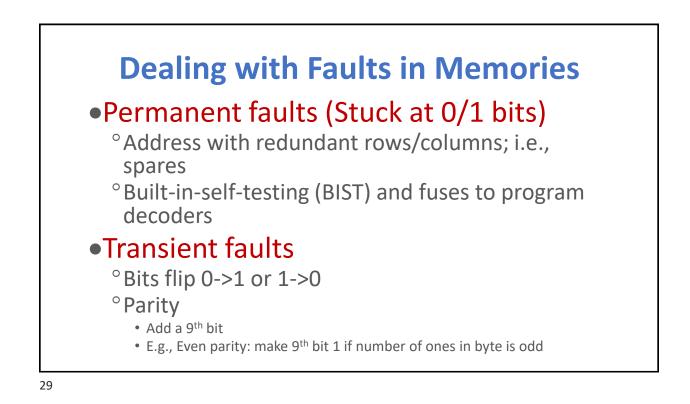


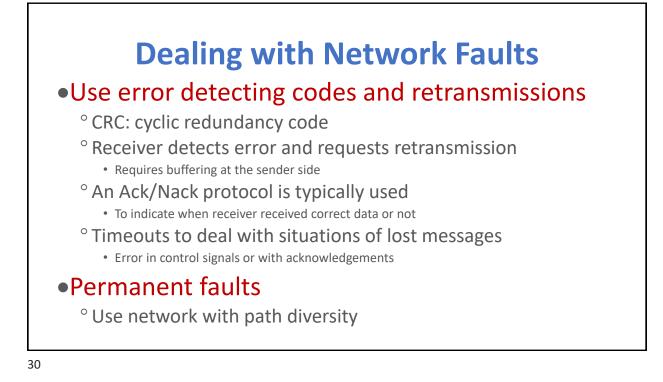




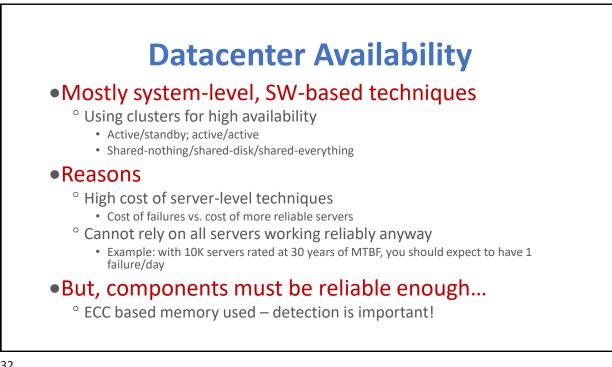


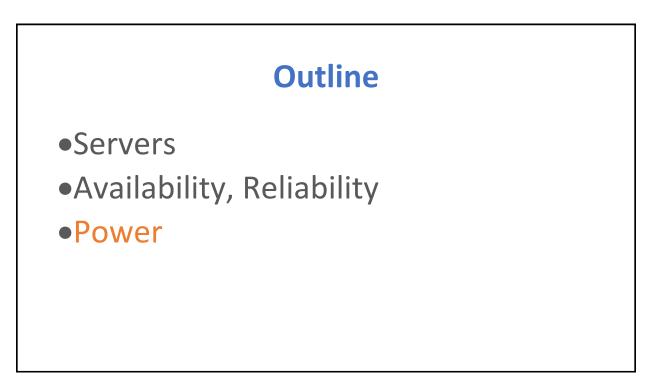
		of inexpensive disks (RAID)
	ion of disks t lth, high relia	that behaves like a single disk with: High capacity, high
		or correcting information across disks
GranulAmour	arity of the interl nt and distribution	two distinguishing features: leaving (bit, byte, block) n of redundant information tion – RAID levels 0 to 6:
	Level	Description
	RAID0	Block-level striping without parity mirroring
	RAID 1	Mirroring without parity striping
	RAID 1 RAID 2	Mirroring without parity striping Bit-level striping with dedicated parity
	RAID 2	Bit-level striping with dedicated parity
	RAID 2 RAID 3	Bit-level striping with dedicated parity Byte-level striping with dedicated parity
	RAID 2 RAID 3 RAID 4	Bit-level striping with dedicated parity Byte-level striping with dedicated parity Block-level striping with dedicated parity

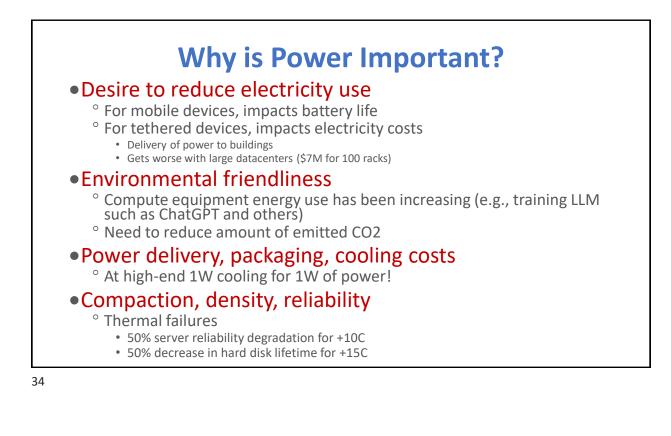


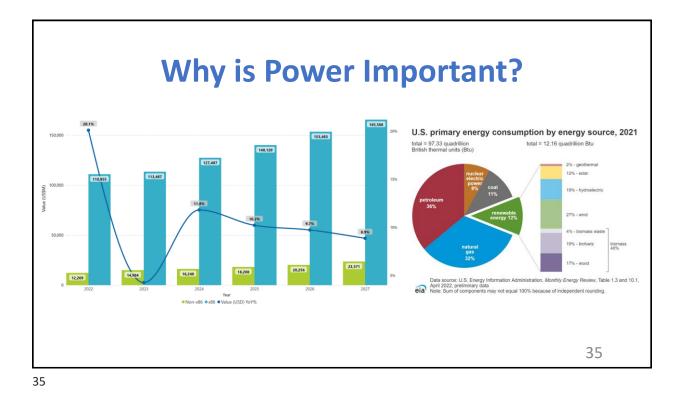


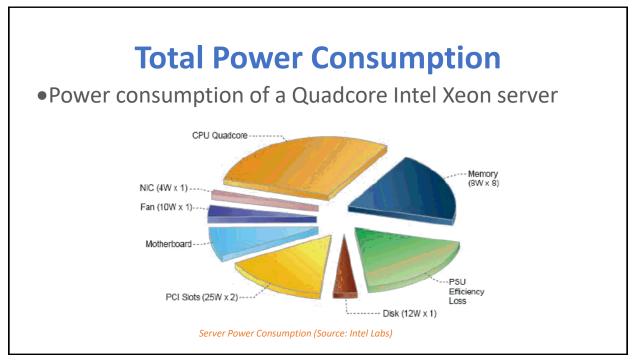
	aling with Faults in Logic
•Triple mod	ular redundancy (TMR)
° Three copie	s of compute unit + majority voter
° Issues: sync	hronization & common mode errors
• Dual modu	lar redundancy (DMR)
° Two copies	of compute unit + comparator
° Can use sim	pler 2 nd copy (e.g., parity detector)
 Checkpoint 	: & restore
° Periodic che	eckpoints of state
° On error de	tection, rollback & re-execute from checkpoint
 Issues: check recovery tin 	kpoint interval, detection speed, number of checkpoints, ne,

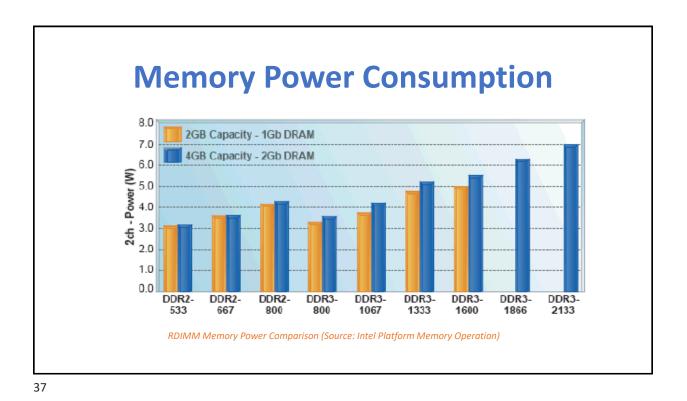


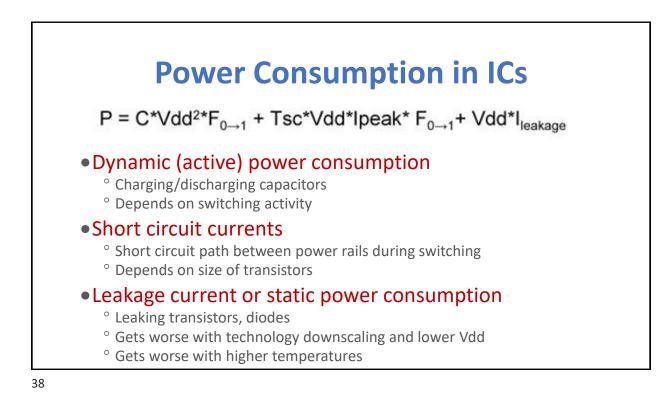


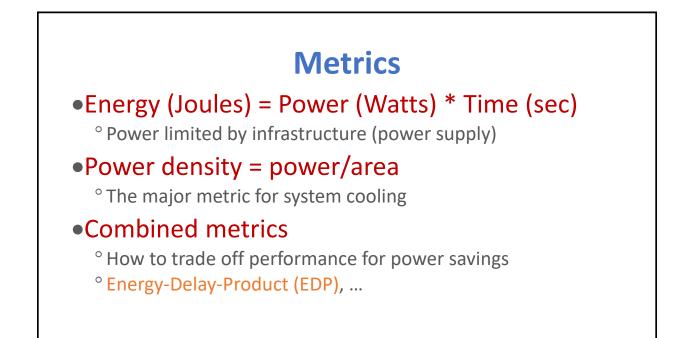




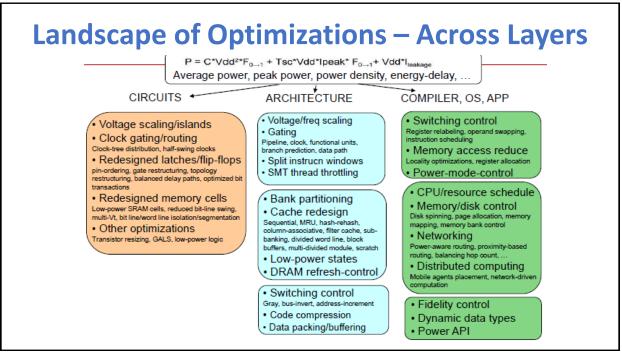


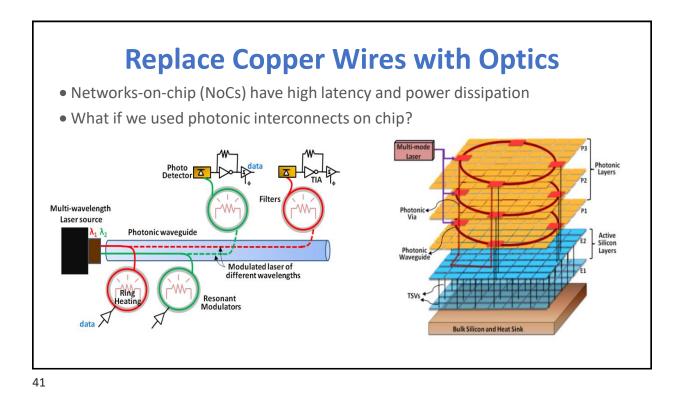


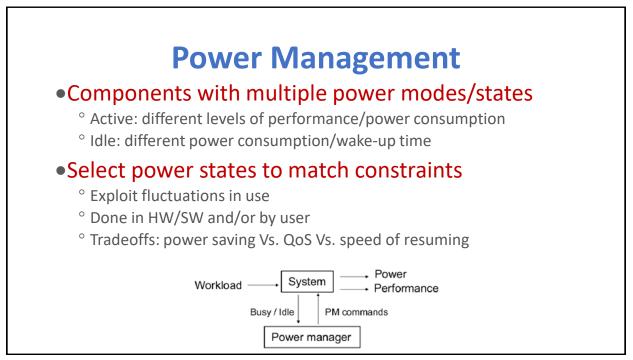


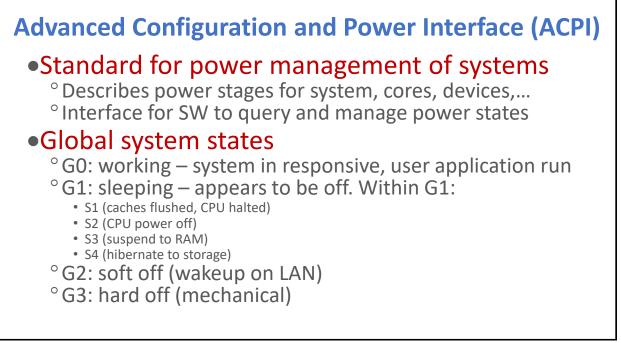














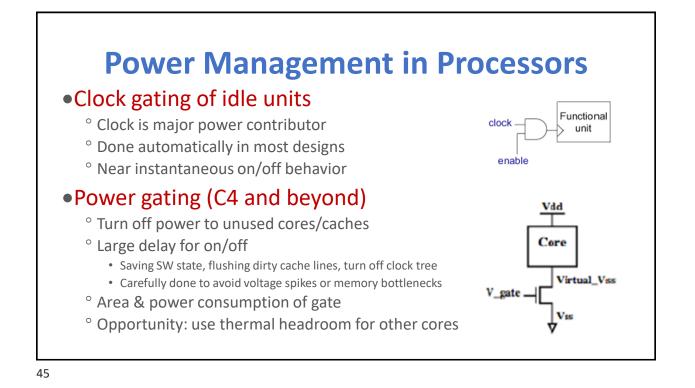
Advanced Configuration and Power Interface (ACPI)

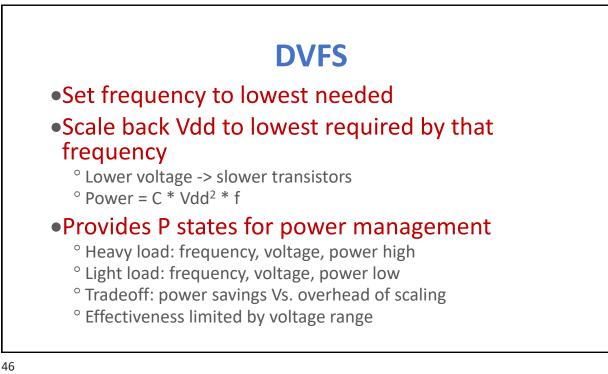
Device states

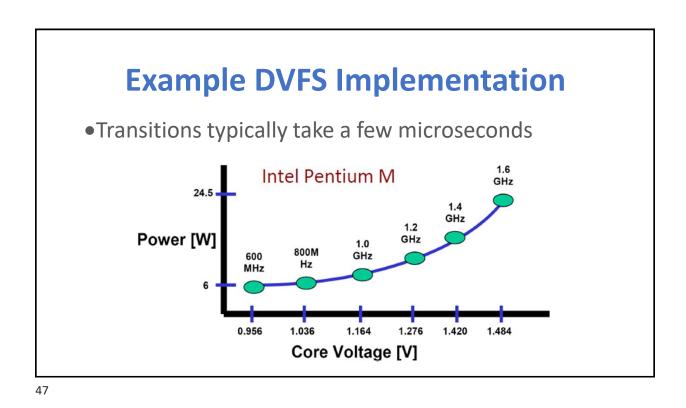
- ° D0 fully on operating state
- ° D1 and D2 are intermediate states (vary by design)
- ° D3 is powered off state (device unresponsive)

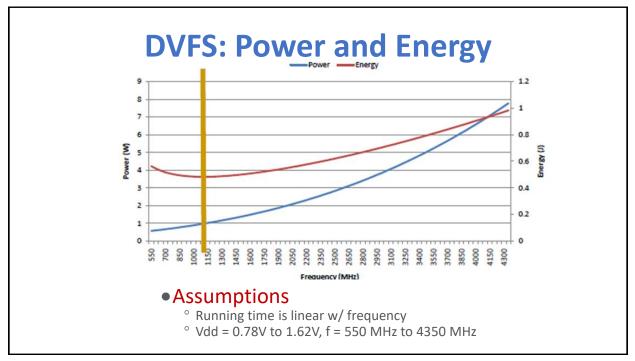
Processor states

- $^{\circ}$ C0 is fully on
- ° With P states related to DVFS stages
- $^\circ$ C1 to C3 are idle modes
- ° Clock may be stopped, but, state is maintained
- ° C4 and beyond are various power off state
- ° First the cache, then cores, and finally the whole chip









DRAM Power States

Power State	Operating Mode	Resync -time	% Active power
Active	All modules ready	0 cycles	100%
Standby	Column multiplexers disabled	2 cycles	60%
Napping	Row decoders turned off	30 cycles	10%
Power Down	Clock sync to Controller interface turned off	9000 cycles	1%
Disabled	No refresh; data lost	Reboot	0%

•Example: 5 states in DR-DRAM

•Tradeoff: power savings Vs. resync penalty

